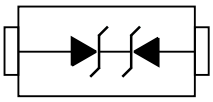
Description

The PTVSHC1SF12VBH ESD protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC1SF12VBH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC1SF12VBH is available in a SOD-123FL package with working voltages of 12 volt. It is used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge)



Feature

- 4000W Peak pulse power per line (t_P = 8/20μs)
- SOD-123FL package
- Response time is typically < 1 ns
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 80A (5/50ns)

Applications

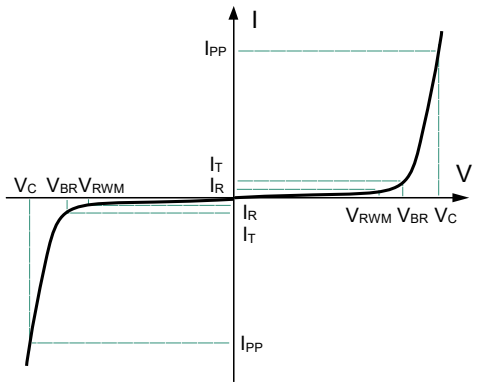
- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260℃
- Pure tin plating: 7 ~ 17 um
- Pin flatness：≤3mil

Electronics Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				12	V
Breakdown Voltage	V_{BR}	I_t =1mA	13	14	15	V
Reverse Leakage Current	I_R	V_{RWM} =12V			1	μA
Clamping Voltage	V_C	I_{PP}=90A t_P = 8/20μs		21	24	V
Clamping Voltage	V_C	I_{PP}=140A t_P = 8/20μs		24	27	V
Clamping Voltage	V_C	I_{PP}=160A t_P = 8/20μs		25	28	V
Junction Capacitance	C_j	V_R=0V f = 1MHz		430	550	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t_P = 8/20μS)	P_{pp}	4000	W
Lead Soldering Temperature	T_L	260 (10 sec)	℃
Operating Temperature	T_J	-55 to +125	℃
Storage Temperature	T_{STG}	-55 to +150	℃

Typical Characteristics

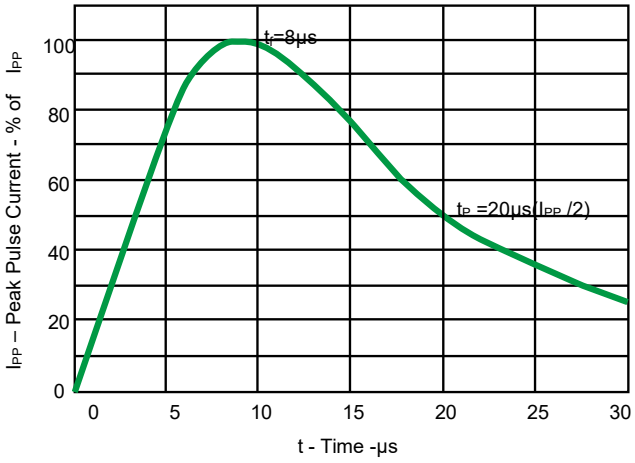


Fig 1.Pulse Waveform

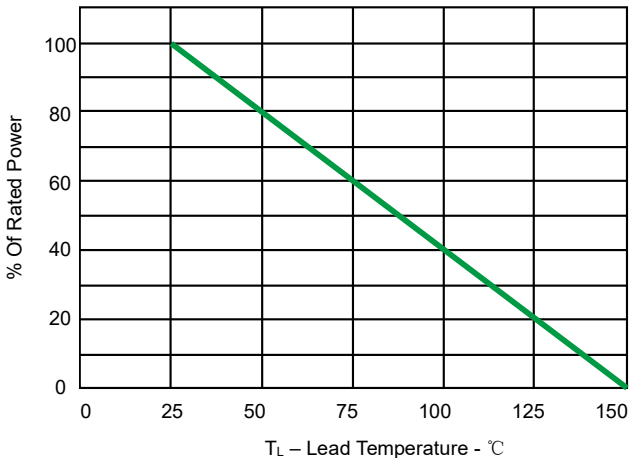


Fig 2.Power Derating Curve

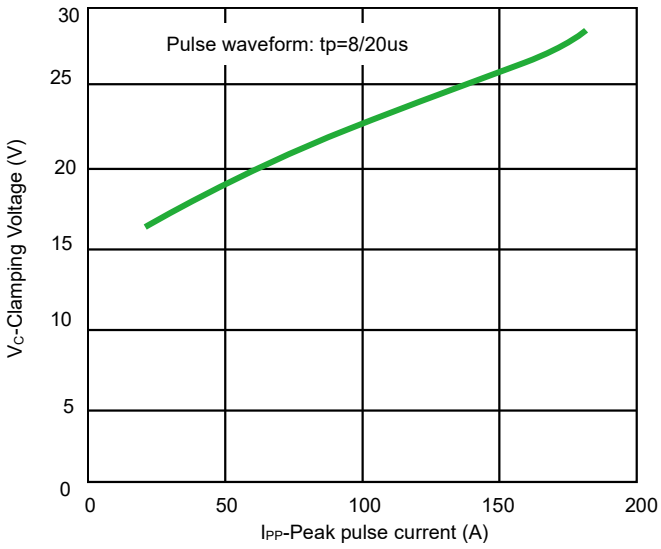


Fig 3. Clamping voltage vs. Peak pulse current

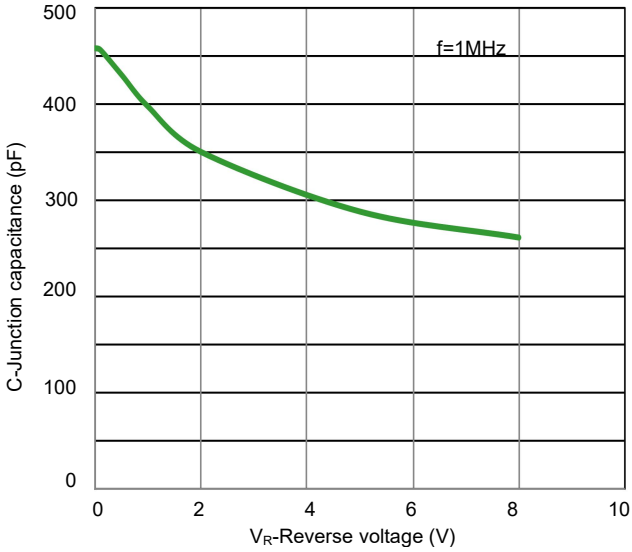


Fig 4. Capacitance vs. Reveres voltage

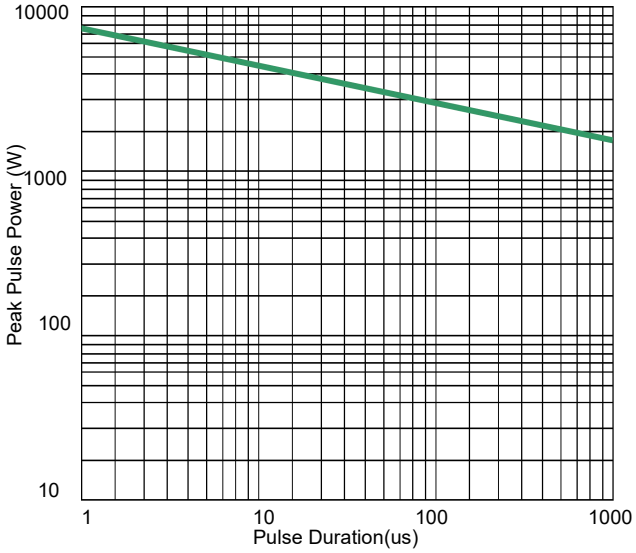
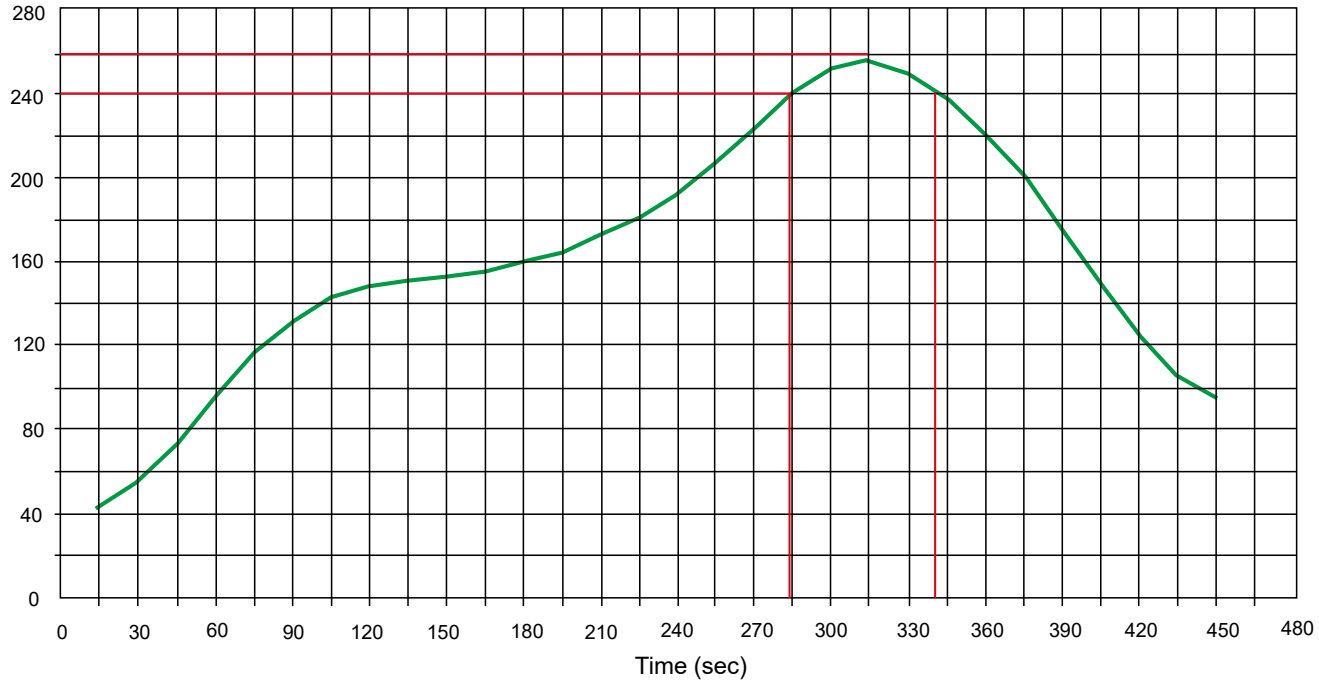


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

Solder Reflow Recommendation

Peak Temp=257℃, Ramp Rate=0.802deg. ℃/sec

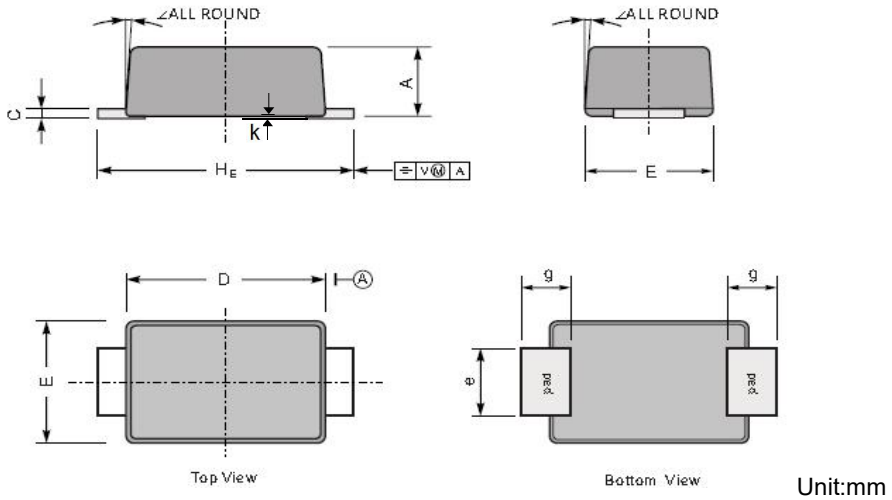


PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

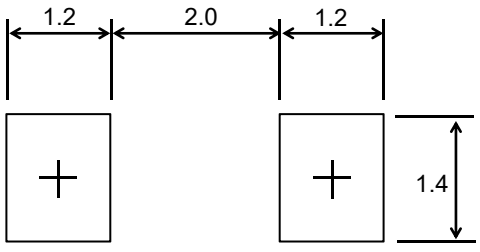
- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (SOD-123FL)



Unit:mm

Dim	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.031	0.039	0.80	0.98
C	0.002	0.010	0.05	0.25
H_E	0.138	0.154	3.50	3.90
E	0.061	0.077	1.55	1.95
D	0.098	0.114	2.50	2.90
g	0.020	0.043	0.50	1.10
e	0.024	0.039	0.60	1.00
k	0.004		0.10	
∠	7°			



Suggested PCB Layout Unit:mm

Marking information

Ordering information

Device	Package	Reel	Shipping
PTVSHC1SF12VBH	SOD-123FL (Pb-Free)	7"	3000 / Tape & Reel

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