

Description

The PESDNC2FD8VBA protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

Feature

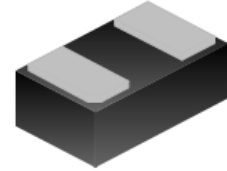
- 110W peak pulse power per line ($t_P = 8/20\mu s$)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically $< 1ns$
- Low clamping voltage
- Transient protection for data lines to
IEC61000-4-2(ESD) $\pm 30kV$ (air), $\pm 30kV$ (contact);
IEC61000-4-4 (EFT) 40A (5/50ns)

Applications

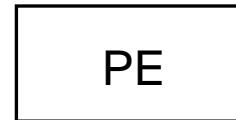
- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Mechanical Characteristics

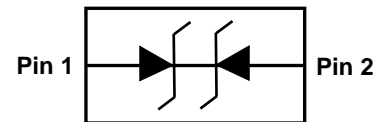
- Mounting position: Any
- Qualified max reflow temperature: $260^{\circ}C$
- Device meets MSL 1 requirements
- DFN1006-2L without plating
- RoHS compliant



DFN1006-2L(Bottom View)



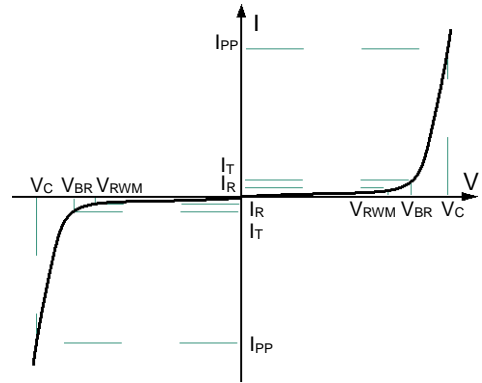
Marking (Top View)



Circuit Diagram

Electronics Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Peak Reverse Working Voltage	V_{RWM}				8	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	8.5		12.5	V
Reverse Leakage Current	I_R	$V_{RWM} = 8\text{V}$			1	μA
Clamping Voltage ⁽¹⁾	V_C	TLP=16A $t_p=100\text{ns}$		18		V
Dynamic Resistance ⁽¹⁾	R_{DYN}			0.43		Ω
Clamping Voltage ⁽²⁾	V_C	$I_{PP}=3\text{A}$ $t_p = 8/20\mu\text{s}$		11.5	14	V
		$I_{PP}=8\text{A}$ $t_p = 8/20\mu\text{s}$		14.5	18	V
Junction Capacitance	C_J	$V_R=0\text{V}$ $f = 1\text{MHz}$		21		pF

Notes:

1. TLP Parameter: $Z_0=50\Omega, t_p=100\text{ns}, t_r=2\text{ns}$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
2. Non-repetitive current pulse, according to IEC61000-4-5.

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu\text{s}$)	P_{pp}	110	W
Peak Pulse Current ($t_p=8/20\mu\text{s}$)	I_{PP}	8	A
Operating Temperature	T_J	-55 to 150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^{\circ}\text{C}$
ESD Protection-Contact Discharge	V_{ESD}	± 30	kV
ESD Protection-Air Discharge	V_{ESD}	± 30	kV

Typical Characteristics

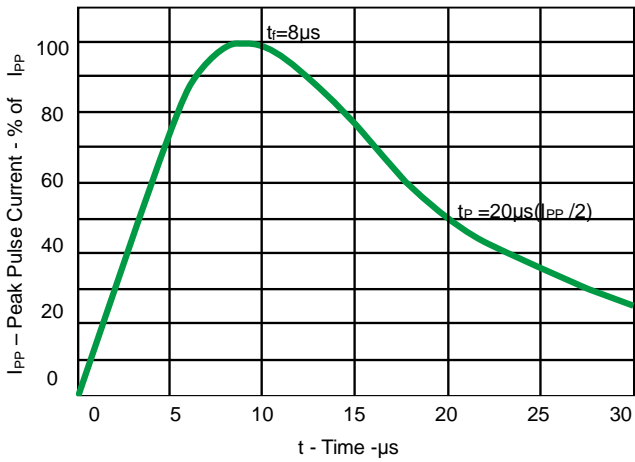


Fig 1. Pulse Waveform

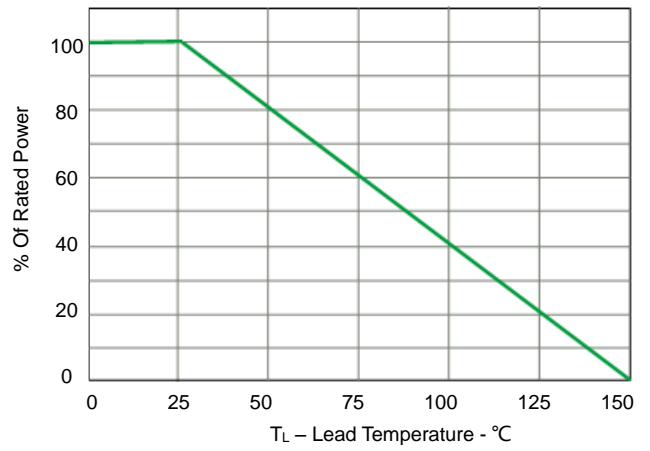


Fig 2. Power Derating Curve

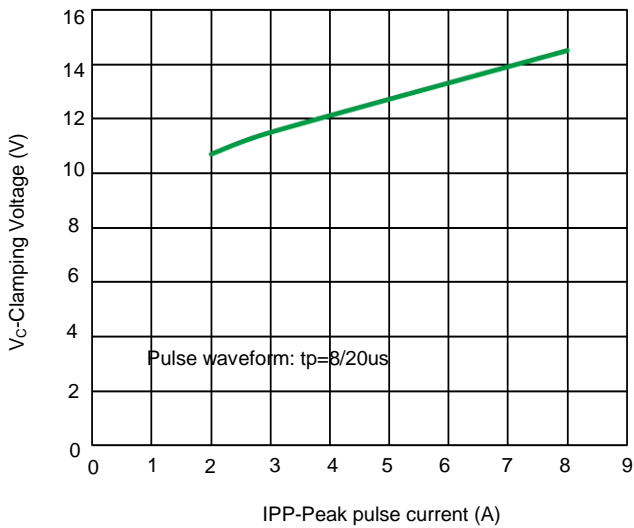


Fig 3. Clamping voltage vs. Peak pulse current

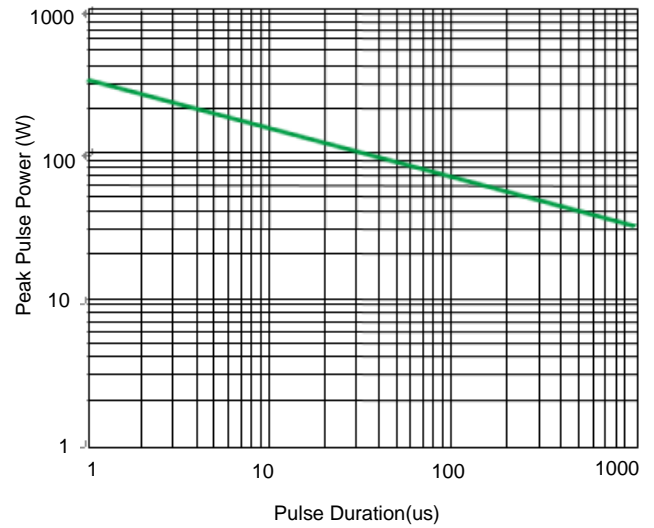


Fig 4. Non-Repetitive Peak Pulse Power vs. Pulse time

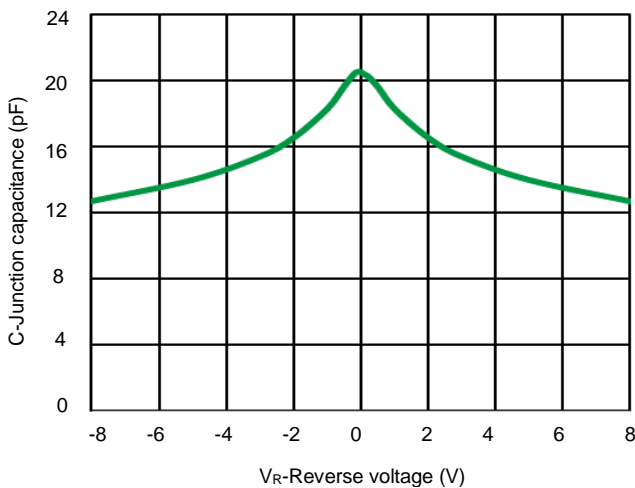


Fig 5. Capacitance vs. Reverse voltage

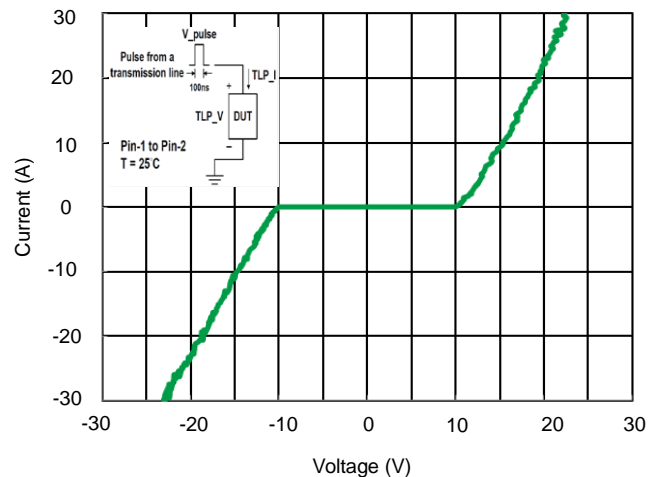


Fig 6. TLP Measurement

ESD Protector

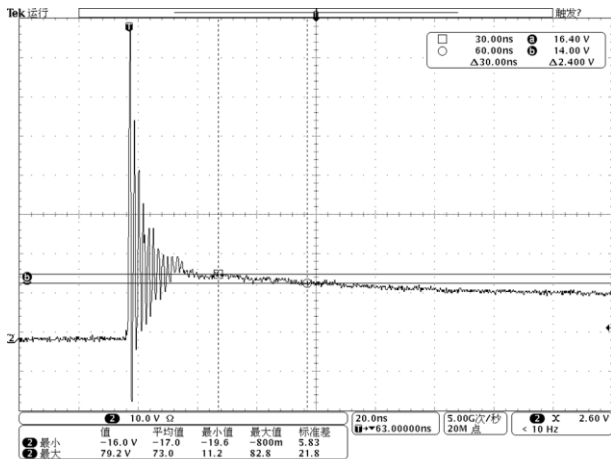


Fig 7. ESD clamping voltage
(IEC61000-4-2 +8kV contact)

PESDNC2FD8VBA

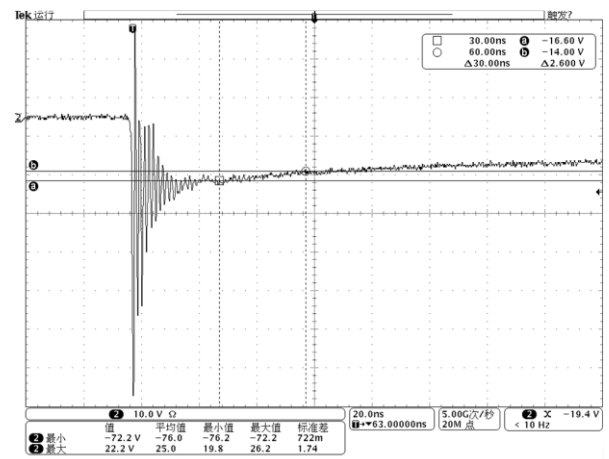
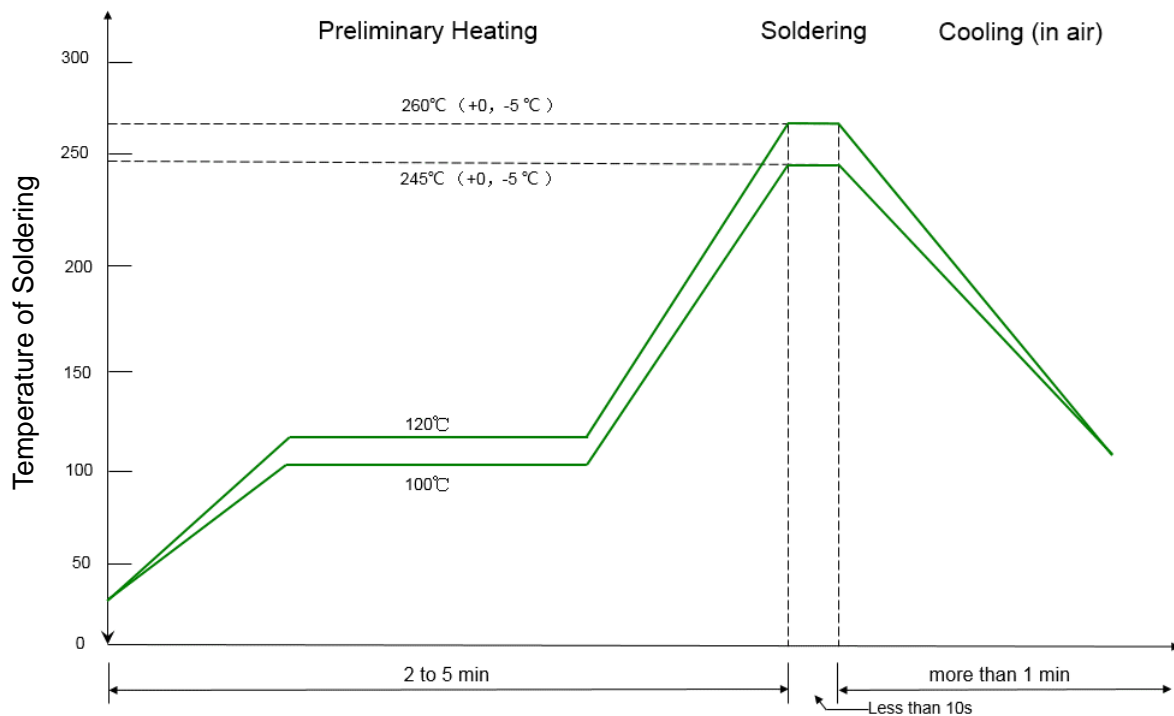


Fig 8. ESD clamping voltage
(IEC61000-4-2 -8kV contact)

Solder Reflow Recommendation



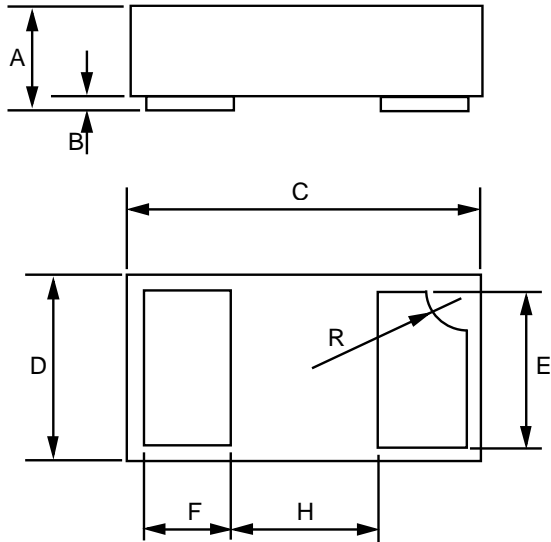
Remark: Pb free for 260°C; Pb for 245°C.

PCB Design

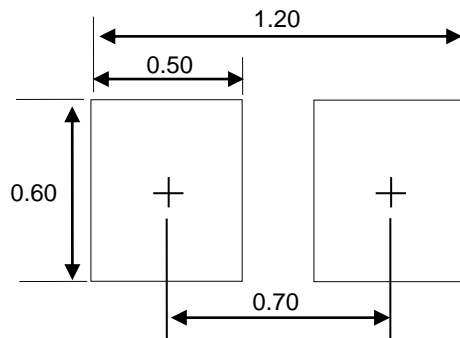
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN1006-2L)



Dim	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.013	0.020	0.34	0.50
B	0.000	0.002	0.00	0.05
C	0.037	0.043	0.95	1.080
D	0.022	0.027	0.55	0.680
E	0.016	0.024	0.40	0.60
F	0.008	0.012	0.20	0.30
H	0.015Typ.		0.40Typ.	
R	0.001	0.005	0.05	0.15



Unit:mm

Suggested PCB Layout

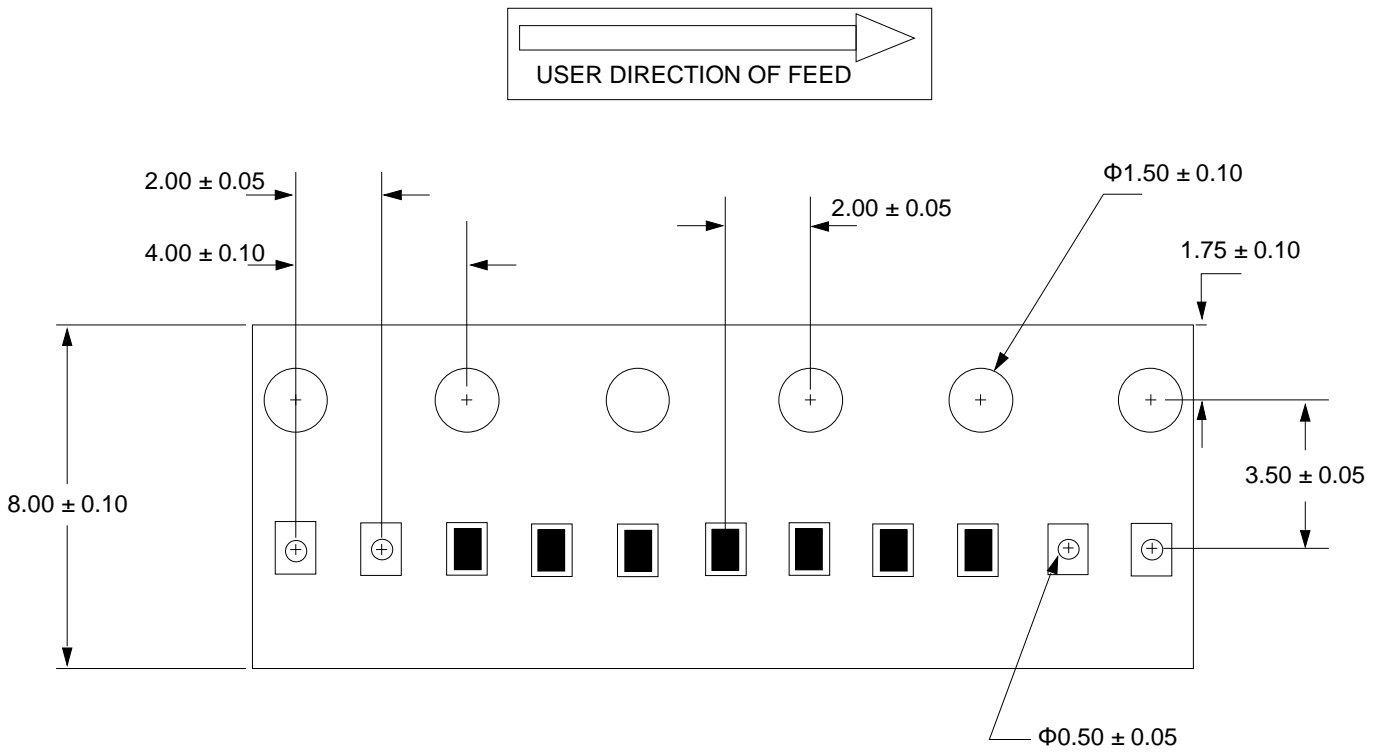
Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.


Ordering information

Device	Package	Reel	MPQ
PESDNC2FD8VBA	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

Load with information




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