

Description

Product Summary		
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ)	I _D (A)
650	50@ V _{GS} = 12V	38

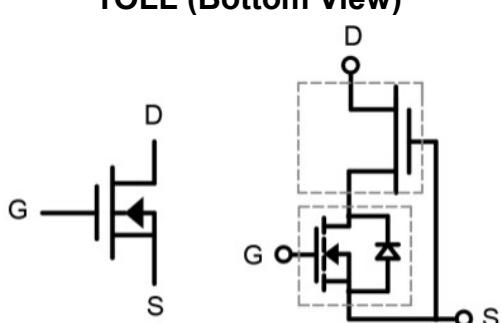


Feature

- Easy to use, compatible with standard gate drivers
- Excellent Q_G x R_{DS(on)} figure of merit (FOM)
- Low Q_{RR}, no free-wheeling diode required
- Low switching loss
- RoHS compliant and Halogen-free

Applications

- High efficiency power supplies
- Telecom and datacom
- Automotive
- Servo motors



Schematic Diagram

Absolute maximum rating@25°C

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	650	V
Gate-Source Voltage	V _{GS}	±20	V
Transient Drain-Source Voltage ¹⁾	V _{TDS}	800	V
Continuous Drain Current	T _C =25°C	I _D	A
	T _C =100°C		
Pulsed Drain Current (Pulse Width: 100μs)	T _C =25°C	I _{DM}	A
	T _C =100°C		
Power Dissipation	P _D	139	W
Soldering Peak Temperature	T _{CSOLD}	260	°C
Operating Junction and Storage Temperature	T _J , T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	0.91	-	°C/W
Thermal Resistance, Junction-to-Ambient ²⁾	R _{θJA}	-	50	-	°C/W

650V GaN Power Transistor

PGCTL65R50A

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units	
Statistic Characteristics								
Maximum Drain-Source Voltage	V _{DS-Max}	V _{GS} = 0V		650	-	-	V	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250µA		-	1000	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =700V, V _{GS} =0V	T _J =25°C	-	10	30	µA	
			T _J =150°C	-	50	-		
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V		-	-	±150	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 500µA	3.0	4.0	5.0	-	V	
Gate Threshold Voltage Temperature Coefficient	△V _{GS(th)} /T _J			-	-11.3	-	mV/°C	
Drain-Source On-State Resistance ³⁾	R _{DS(ON)}	V _{GS} =12V, I _D =4A	T _J =25°C	-	50	65	mΩ	
			T _J =150°C	-	100	-		
Dynamic Characteristics								
Input Capacitance	C _{iss}	V _{DS} = 400V, V _{GS} = 0V, f = 1MHz	V _{GS} = 0V, V _{DS} = 0-400V	-	519	-	pF	
Output Capacitance	C _{oss}			-	117	-		
Reverse Transfer Capacitance	C _{rss}			-	2.7	-		
Effective Output Capacitance, Energy Related	C _{o(er)}	V _{GS} = 0V, V _{DS} = 0-400V	V _{GS} = 0V, V _{DS} = 0-400V	-	175	-	pF	
Effective Output Capacitance, Time Related	C _{o(tr)}			-	317	-		
Output Charge	Q _{oss}			-	127	-	nC	
Turn-on Delay Time	t _{d(on)}	V _{DS} = 400V, I _D = 10A, V _{GS} = 0-12V, R _G = 40Ω	V _{GS} = 0V, V _{DS} = 0-400V	-	44	-	ns	
Turn-on Rise Time	t _r			-	16	-		
Turn-Off Delay Time	t _{d(off)}			-	40	-		
Turn-Off Fall Time	t _f			-	12	-		
Total Gate Charge	Q _g	V _{DS} = 400V, I _D = 6A, V _{GS} = 0-12V	V _{GS} = 0V, V _{DS} = 0-400V	-	17	-	nC	
Gate-Source Charge	Q _{gs}			-	4.6	-		
Gate-Drain Charge	Q _{gd}			-	5.6	-		
Reverse Diode Characteristics								
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =12.5A		-	1.3	-	V	
		V _{GS} =0V, I _S =25A	T _J =25°C	-	1.9	-		
			T _J =150°C	-	3.0	-		
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _S =25A, V _{DD} =400V, di/dt=1000A/µs		-	43	-	ns	
Reverse Recovery Charge	Q _{rr}			-	127	-	µC	

Notes:

1. Off-state spike duty cycle < 0.01, spike duration < 2µs
2. Device on one layer epoxy PCB for drain connection (vertical and without air stream cooling, with 6cm²copper area and 70µm thickness)
3. Dynamic on-resistance; see Figure 18 and 19 for test circuit and configurations

Typical Characteristics

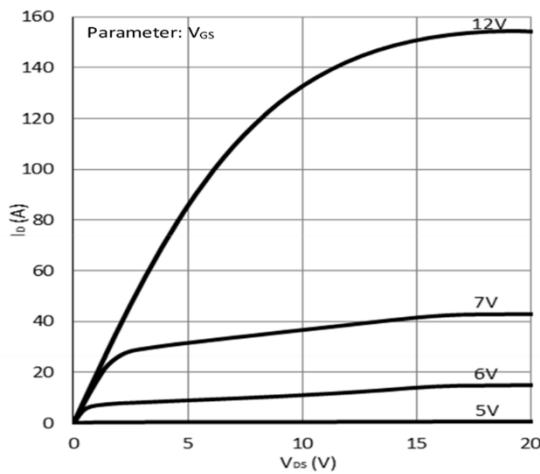
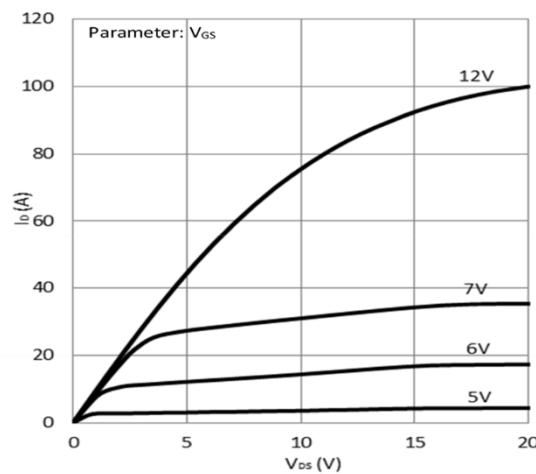
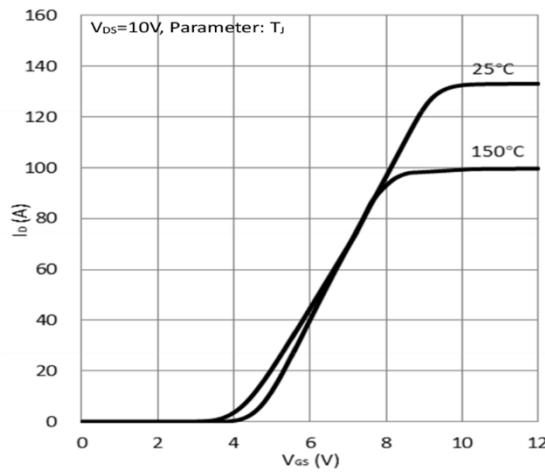
Figure 1. Typical Output Characteristics $T_j=25^\circ\text{C}$ Figure 2. Typical Output Characteristics $T_j=150^\circ\text{C}$ 

Figure 3. Typical Transfer Characteristics

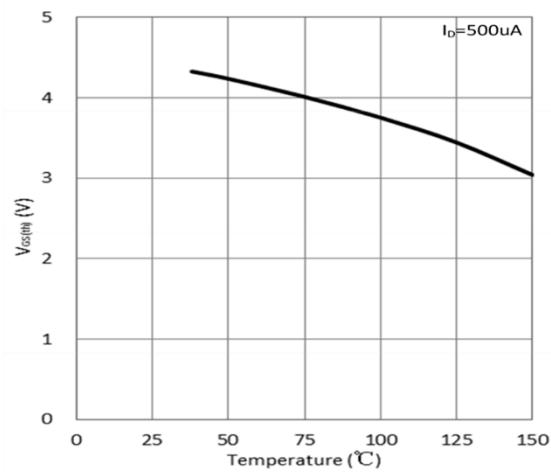
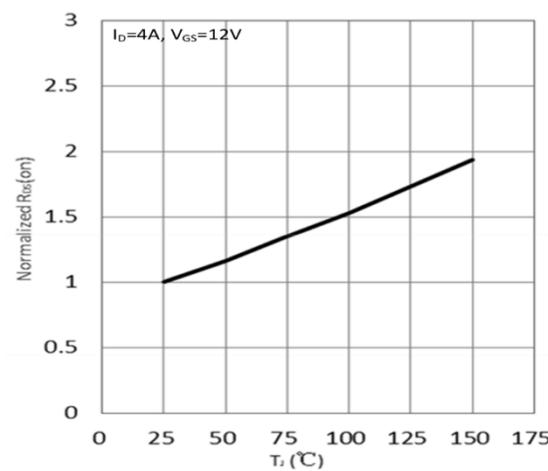
Figure 4. $V_{GS(\text{th})}$ Vs Temperature Characteristics

Figure 5. Normalized On-resistance

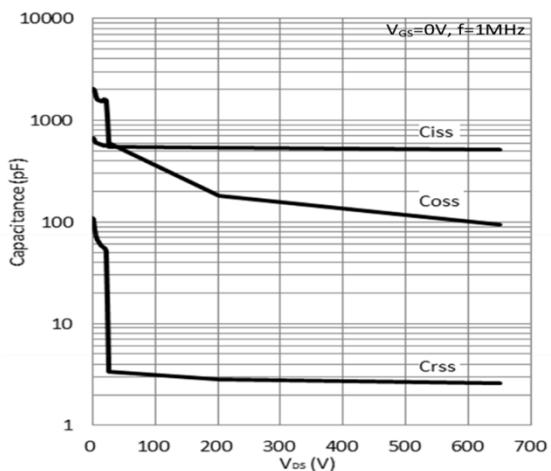


Figure 6. Typical Capacitance

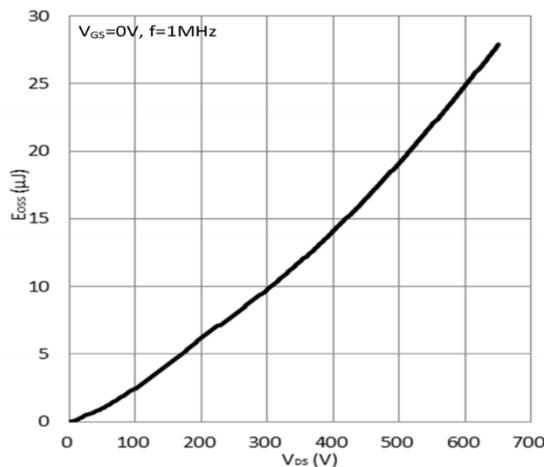


Figure 7. Typical Coss Stored Energy

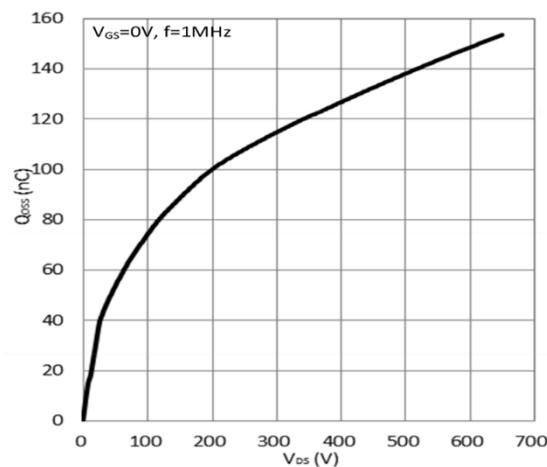


Figure 8. Typical Qoss

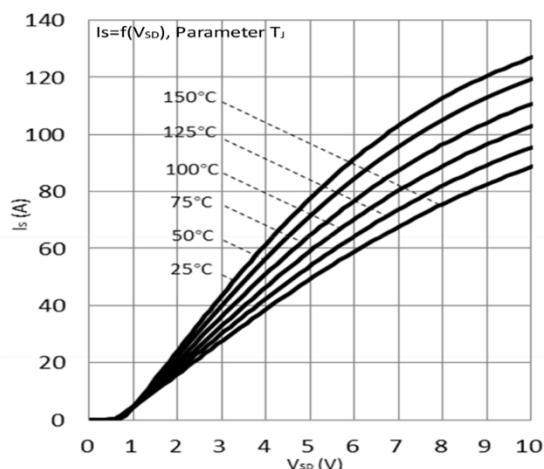


Figure 9. Forward Characteristic of Rev. Diode

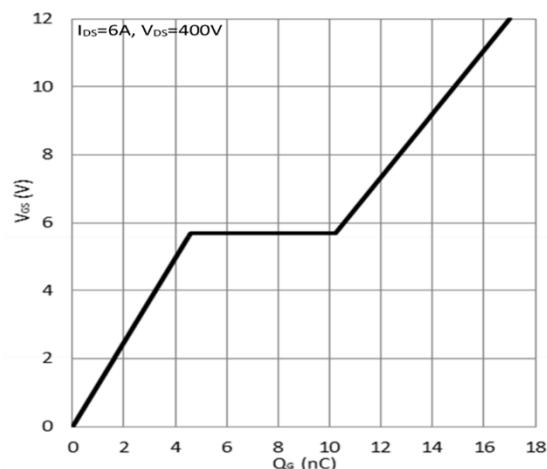


Figure 10. Typical Gate Charge

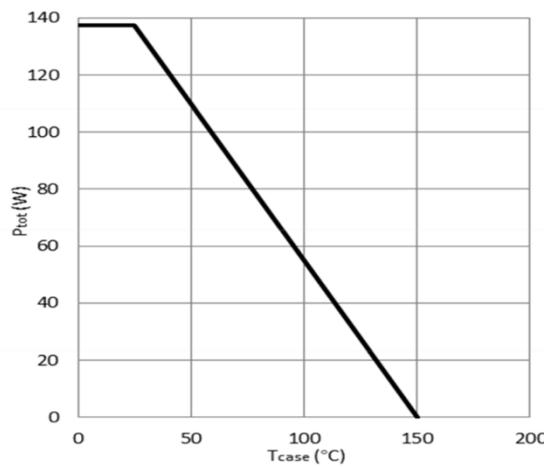


Figure 11. Power Dissipation

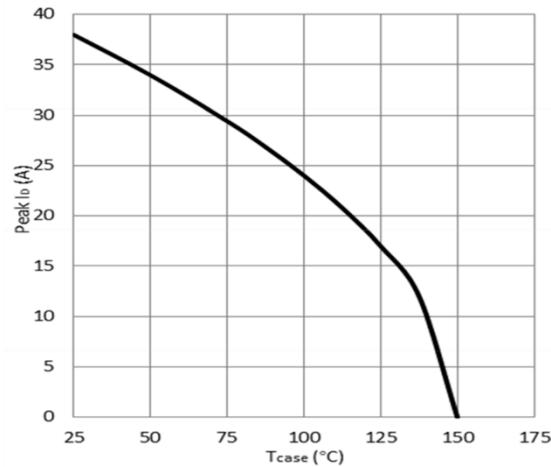


Figure 12. Current Derating

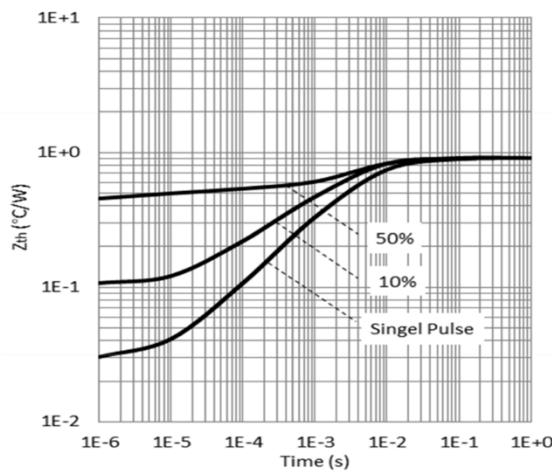


Figure 13. Transient Thermal Resistance

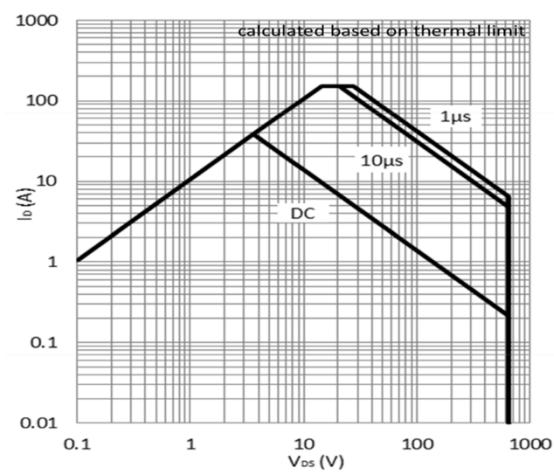


Figure 14. Safe Operating Area $T_c=25^\circ\text{C}$

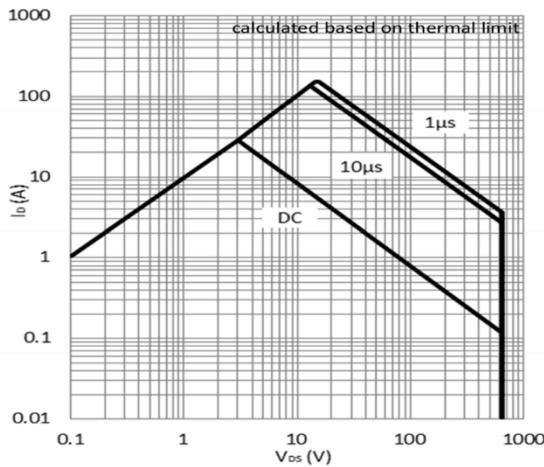


Figure 15. Safe Operating Area $T_c=80^\circ\text{C}$

Test Circuits and Waveforms

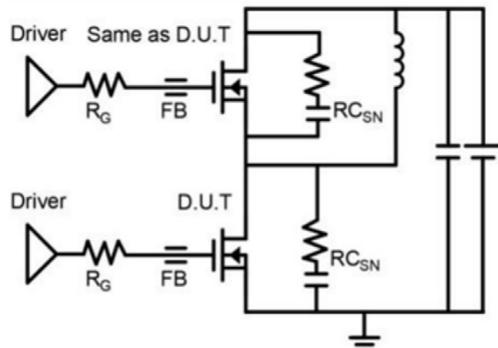


Figure 16. Switching Time Test Circuit

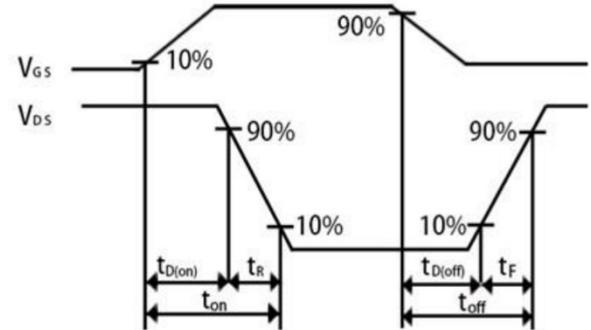


Figure 17. Switching Time Waveform

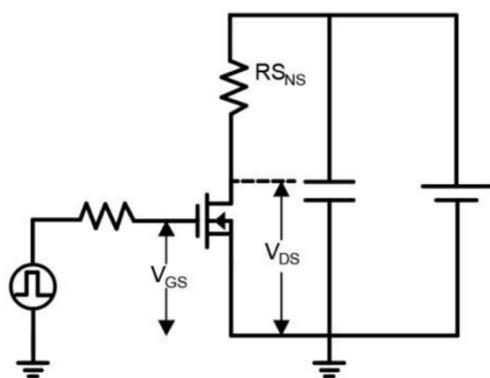
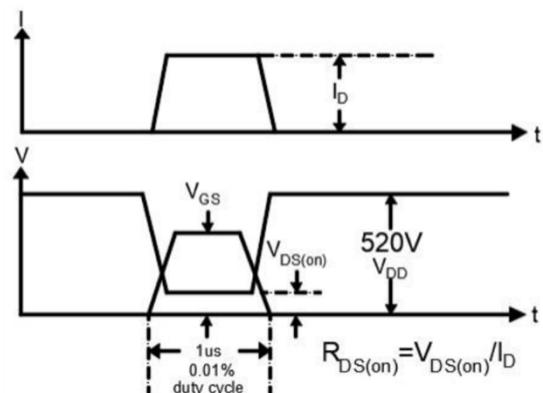
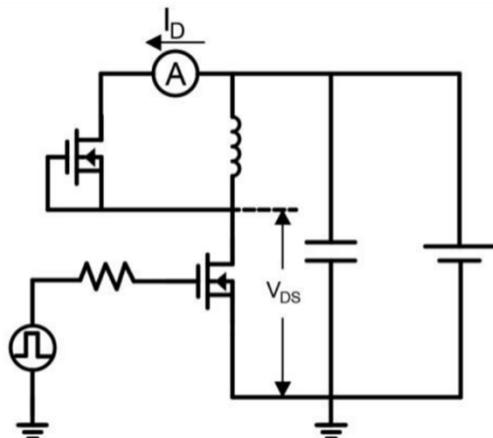
Figure 18. Dynamic $R_{DS(on)}$ Test CircuitFigure 19. Dynamic $R_{DS(on)}$ Waveform

Figure 20. Diode Characteristic Test Circuits

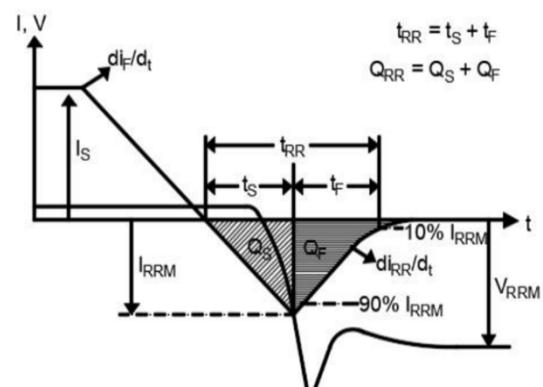
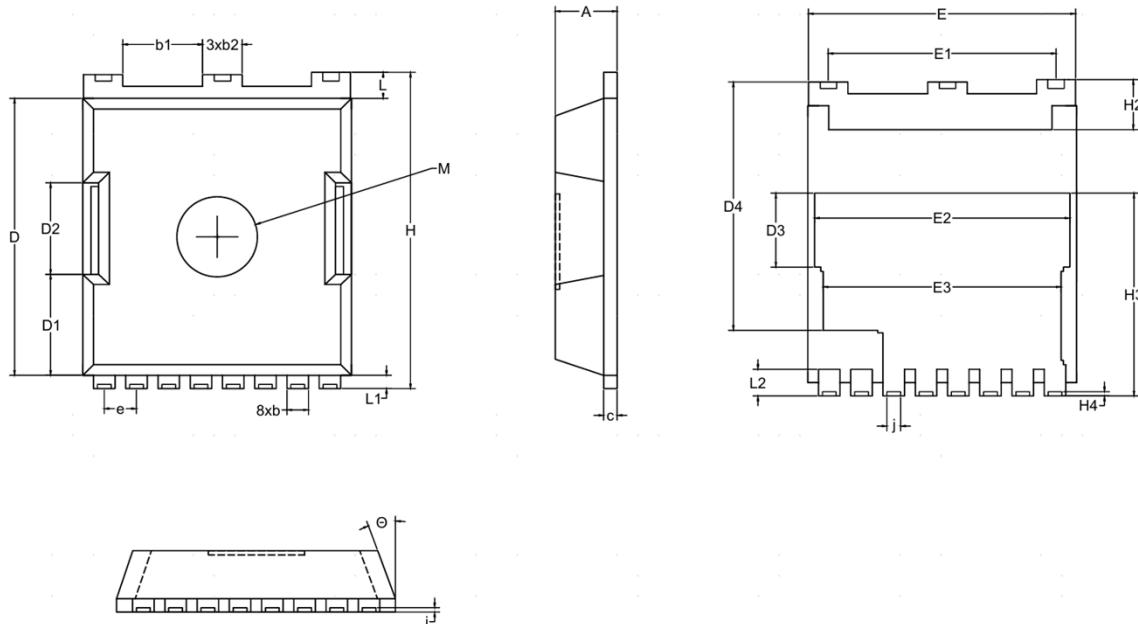


Figure 21. Diode Recovery Waveform

Product dimension (TOLL)



SYMBOL	Millimeter		
	Min	Nom	Max
A	2.20	2.30	2.40
b1	0.70	0.80	0.90
b2	1.10	1.20	1.30
c	0.40	0.50	0.60
D	10.28	10.38	10.58
D1		4.18REF	
D2		3.30 REF	
D3		2.77REF	
D4		9.03REF	
E	9.70	9.90	10.10
E1		8.50REF	
E2		9.40REF	
E3		8.50REF	
e	1.10	1.20	1.30
H	11.48	11.68	11.88
H2	1.10	1.20	1.30
H3	7.50	7.60	7.70
H4	0.13	0.23	0.33
i	0.10	-	-
j	0.42	0.45	0.50
L	0.50	0.70	0.90
L1	0.50	0.60	0.70
L2	1.05	1.20	1.30
M		3REF	
e		10°REF	

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