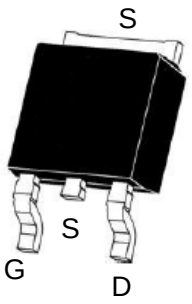
700V Enhancement-mode GaN Transistor

Description

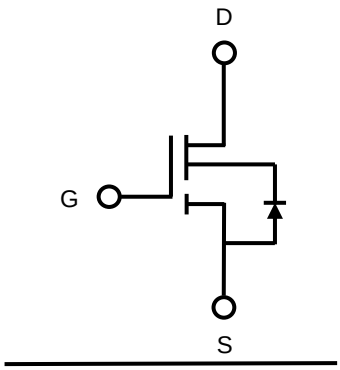
650V Normally-OFF GaN			
V_{DS}(V)	$R_{DS(on)}$(mΩ)	I_{DS}(A)	Q_G(nC)
700	270	7.9	7.9



TO-252 (Top View)

Feature

- Easy to drive—compatible with standard gate drivers
- Low conduction and switching losses
- RoHS compliant and Halogen-free



Circuit Diagram

Applications

- Adapter
- Renewable energy
- Telecom and data-com
- Servo motors
- Industrial
- Automotive

Absolute maximum rating@25℃

Rating		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	700	V
Drain-Source Voltage-transient[1)]		$V_{DS(transient)}$	800	V
Gate-Source Voltage		V_{GS}	-20 to +20	V
Drain Current-Continuous[2)]	T_C =25℃	I_D	7.9	A
	T_C =125℃		3.5	A
Pulse Drain Current (pulse width: 100μs)		I_{DM}	14	A
Maximum Power Dissipation		P_D	32	W
Junction and Storage Temperature Range		T_J,T_{STG}	-55~+150	℃

Notes:
1. In off-state, spike duty cycle D<0.01, spike duration <1μs
2. For increased stability at high current operation.

Thermal characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Thermal Resistance, Junction - Case	$R_{\theta JC}$	-	3.9	-	℃/W

Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V	700	-	-	V
Total Drain Leakage Current	I_{DSS}	V_{DS}= 700V,V_{GS}= 0V, T_J=25℃	-	-	10	μA
		V_{DS}= 700V,V_{GS}= 0V, T_J=150℃	-	-	100	
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}= V_{DS}, I_{DS}= 1mA	3	4	4.8	V
Gate Threshold Voltage Temperature Coefficient	$\triangle V_{GS(th)}/T_J$		-	-7	-	mV/℃
Gate-Source Leakage Current	I_{GSS}	V_{GS}= ±20V	-	-	±100	nA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= 10V, I_D= 1A, T_J=25℃	-	270	320	mΩ
		V_{GS}= 10V, I_D= 1A, T_J=150℃	-	570	-	
Input Capacitance	C_{iss}	V_{DS}= 400V,V_{GS}= 0V, f= 1MHz	-	293	-	pF
Output Capacitance	C_{oss}		-	17	-	
Reverse Transfer Capacitance	C_{rss}		-	3.74	-	
Output Charge	Q_{oss}	V_{GS} =0V, V_{DS} =0V to 400V, f= 1MHz	-	22.2	-	nC
Total Gate Charge	Q_g	V_{GS}=0 to 10V, V_{DS}=400V, I_D=1A	-	7.9	-	nC
Gate-Source Charge	Q_{gs}		-	2.31	-	
Gate-Drain Charge	Q_{gd}		-	1.65	-	
Turn-on Delay Time	$t_{d(on)}$	V_{DS}=400V, V_{GS}=0V to 10V, I_D=2.1A, $R_{G-on(ext)}$=6.8Ω, $R_{G-off(ext)}$=2.2Ω, L=250μH	-	3.2	-	ns
Turn-on Rise Time	t_r		-	5.5	-	
Turn-Off Delay Time	$t_{d(off)}$		-	7.4	-	
Turn-Off Fall Time	t_f		-	27	-	
Reverse Device Characteristics						
Diode Forward Voltage	V_{SD}	V_{GS}= 0V,I_{SD}= 5A	-	2.3	-	V
Reverse Recovery Time	t_{rr}	I_F=10A, V_{DD}=400V, dI_F/dt=165A/μs	-	14	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.5	-	nC

Typical Characteristics

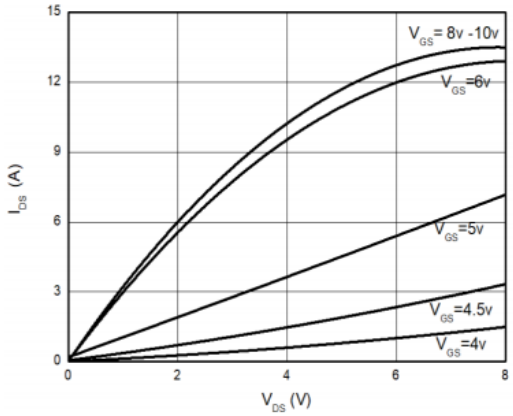


Figure 1. Typical Output Characteristics T_J=25°C

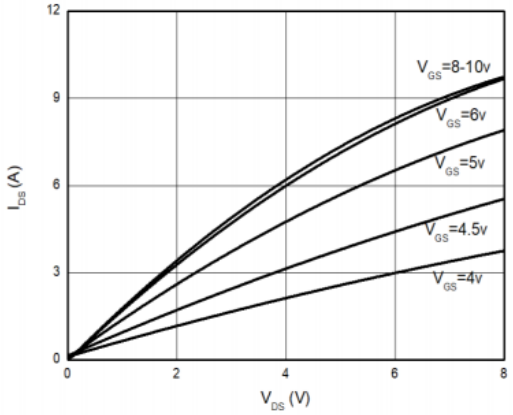


Figure 2. Typical Output Characteristics T_J=125°C

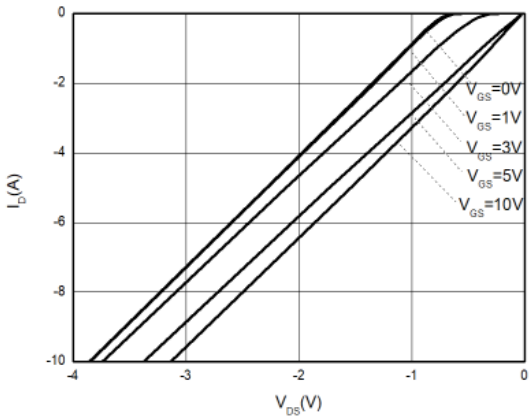


Figure 3. Channel Reverse Characteristics T_J=25°C

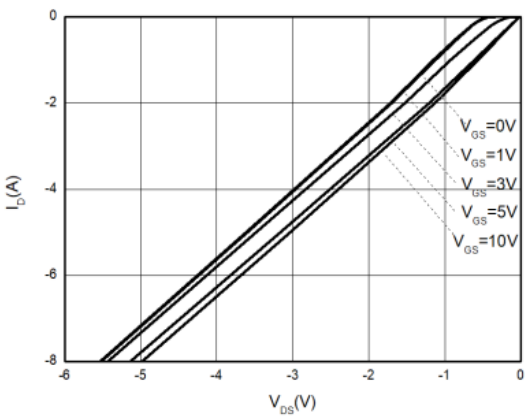


Figure 4. Channel Reverse Characteristics T_J=125°C

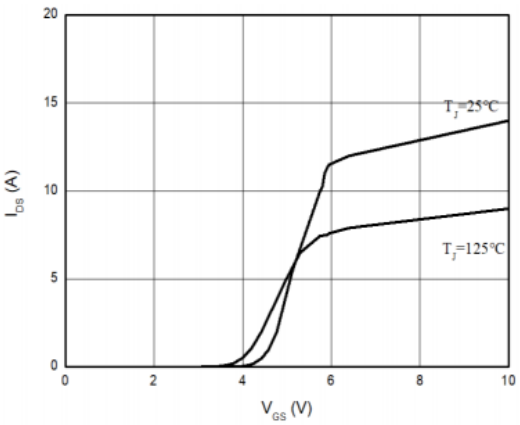


Figure 5. Typical Transfer Characteristics (V_{DS}=5V)

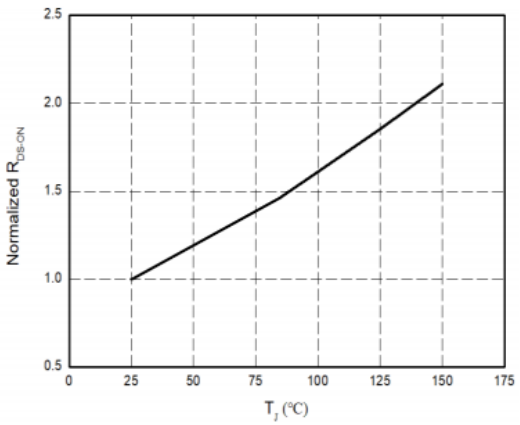


Figure 6. Normalized On-resistance

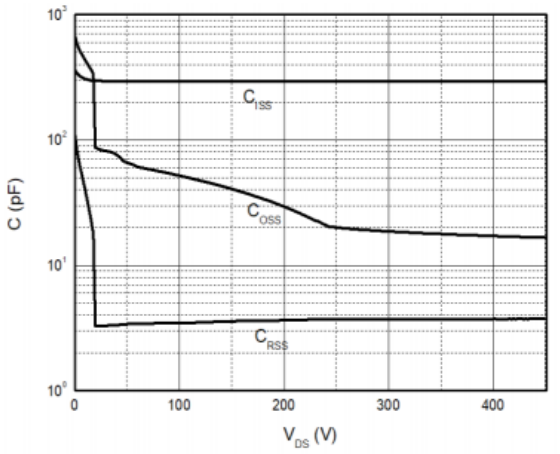


Figure 7. Typical Capacitance (f=1MHz)

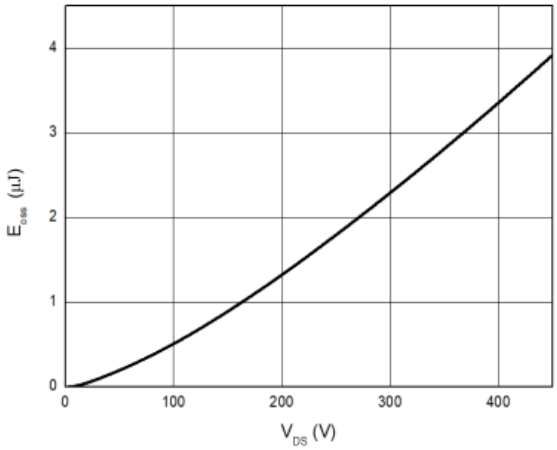


Figure 8. Typical C_{OSS} Stored Energy

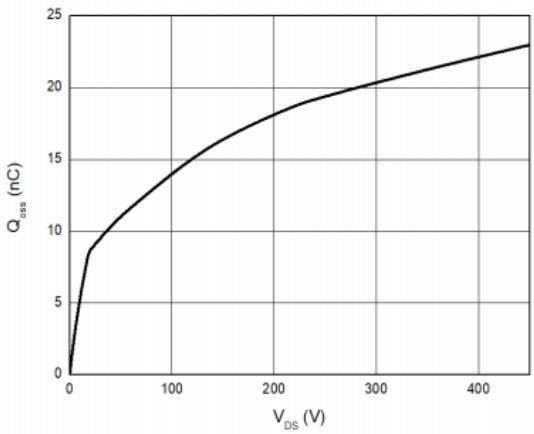


Figure 9. Typical Q_{OSS}

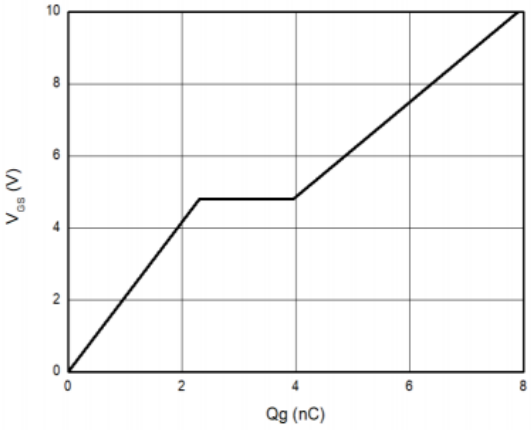


Figure 10. Typical Gate Charge (V_{DS}=400V, I_D=1A)

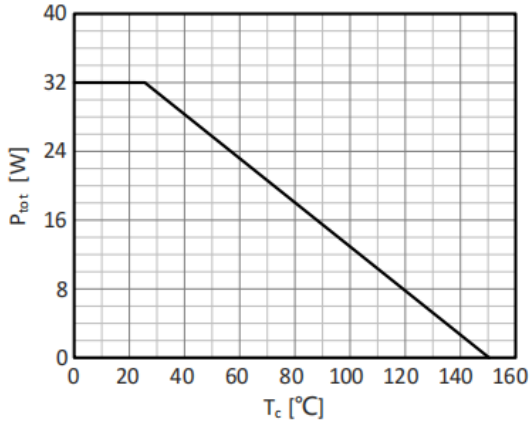


Figure 11. Power Dissipation

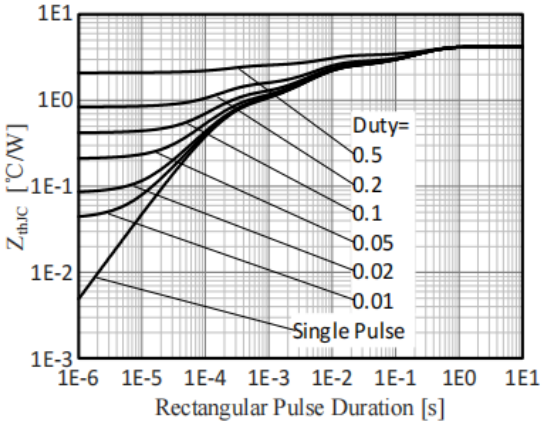


Figure 12. Transient Thermal Resistance

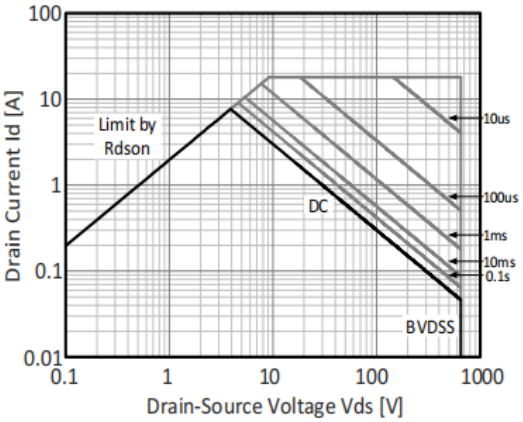


Figure 13. Safe Operating Area T_C=25°C

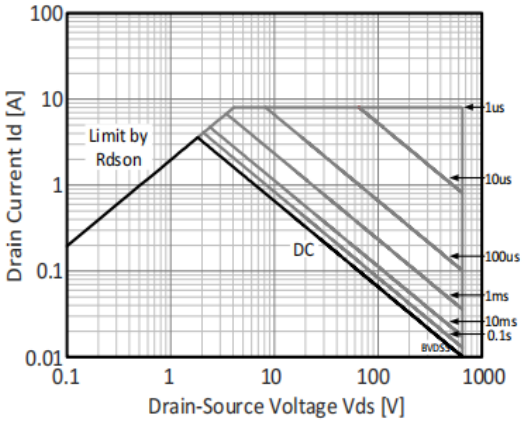
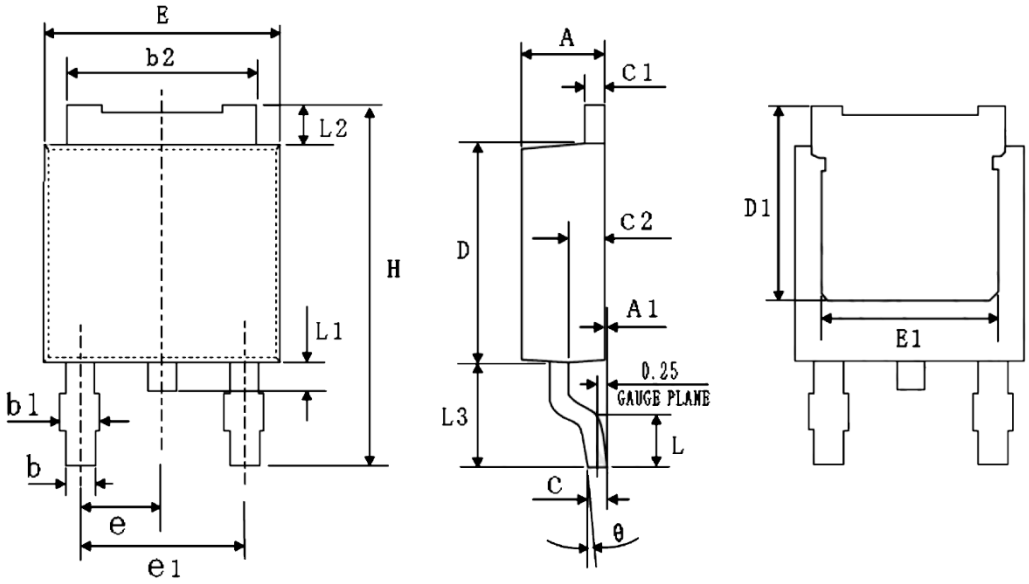
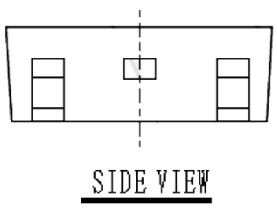


Figure 14. Safe Operating Area T_C=125°C

Product Dimension (TO-252)





SYMBOL	Millimeters			SYMBOL	Millimeters			SYMBOL	Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.40	D1	5.25	5.45	5.65	θ	0°	4°	8°
A1	0.00	0.05	0.10	H	10.00	10.10	10.20	e		2.285 BSC	
b	0.762	0.812	0.862	E	6.50	6.60	6.70				
b 1	--	--	1.10	E1	4.75	4.85	4.95				
b2	5.23	5.33	5.43	e1	4.37	4.57	4.77				
C	0.458	0.508	0.558	L	--	--	1.45				
C1	0.458	0.508	0.558	L1	0.60	0.75	0.90				
C2	0.80	1.00	1.20	L2	0.90	1.10	1.30				
D	6.00	6.10	6.20	L3	2.80	3.00	3.20				

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