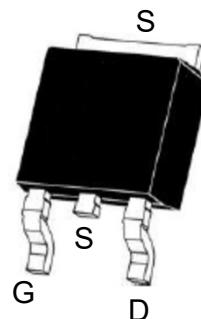


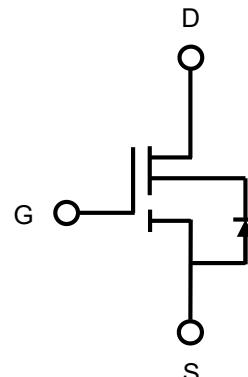
650V Enhancement-mode GaN Transistor

Description

650V Normally-OFF GaN			
V _{DS} (V)	R _{DS(on)} (mΩ)	I _{DS} (A)	Q _G (nC)
650	900	3.6	7.9



TO-252 (Top View)



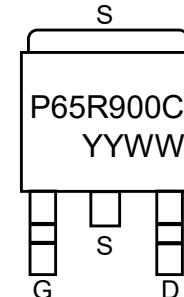
Feature

- Easy to drive compatible with standard gate
- Low conduction and switching losses
- RoHS compliant and Halogen
- Package: TO-252

Applications

- Adapter
- Renewable energy
- Telecom and data-com
- Servo motors
- Industrial
- Automotive

Circuit Diagram



Marking (Top View)

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V _{DS}	650	V
Drain-Source Voltage-transient ¹⁾	V _{DS(transient)}	800	V
Gate-Source Voltage	V _{GS}	-20 to +20	V
Drain Current-Continuous ²⁾	I _D	3.6	A
		1.6	A
Pulse Drain Current (pulse width: 100μs)	I _{DM}	5	A
Maximum Power Dissipation	P _D	21	W
Junction and Storage Temperature Range	T _{J, T_{STG}}	-55~+150	°C

Notes:

1. In off-state, spike duty cycle D<0.01, spike duration <1μs

2. For increased stability at high current operation.

Thermal characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Thermal Resistance, Junction - Case	$R_{\theta JC}$	-	6.0	-	°C/W

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V$	650	-	-	V
Total Drain Leakage Current	I_{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_J = 25°C$	-	-	10	μA
		$V_{DS} = 650V, V_{GS} = 0V, T_J = 150°C$	-	-	100	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}, I_{DS} = 1mA$	3.3	4.0	4.8	V
Gate Threshold Voltage Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		-	-7	-	mV/°C
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 1A, T_J = 25°C$	-	900	1080	mΩ
		$V_{GS} = 10V, I_D = 1A, T_J = 150°C$	-	1900	-	
Input Capacitance	C_{iss}	$V_{DS} = 400V, V_{GS} = 0V, f = 1MHz$	-	293	-	pF
Output Capacitance	C_{oss}		-	17	-	
Reverse Transfer Capacitance	C_{rss}		-	3.74	-	
Output Charge	Q_{oss}	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V, f = 1MHz$	-	22.2	-	nC
Total Gate Charge	Q_g	$V_{GS} = 0 \text{ to } 10V, V_{DS} = 400V, I_D = 1A$	-	7.9	-	nC
Gate-Source Charge	Q_{gs}		-	2.31	-	
Gate-Drain Charge	Q_{gd}		-	1.65	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{DS} = 400V, V_{GS} = 0V \text{ to } 10V, I_D = 2.1A, R_{G-on(ext)} = 6.8\Omega, R_{G-off(ext)} = 2.2\Omega, L = 250\mu H$	-	3.2	-	ns
Turn-on Rise Time	t_r		-	5.5	-	
Turn-Off Delay Time	$t_{d(off)}$		-	7.4	-	
Turn-Off Fall Time	t_f		-	27	-	
Reverse Device Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_{SD} = 2.5A$	-	2.2	-	V
Reverse Recovery Time	t_{rr}	$I_F = 2.5A, V_{DD} = 400V, dI_F/dt = 165A/\mu s$	-	14	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.5	-	nC

Typical Characteristics

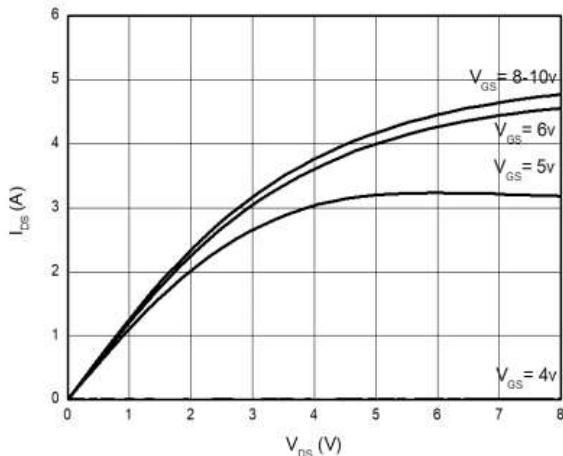
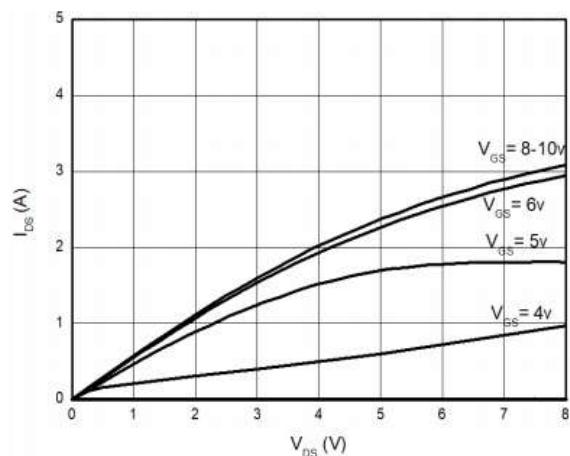
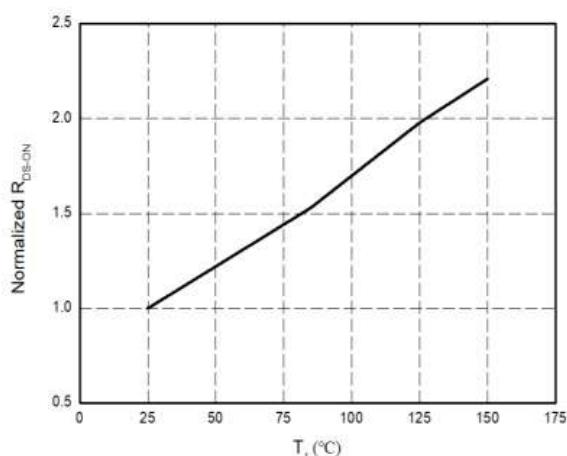
Figure 1. Typical Output Characteristics T_j=25°CFigure 2. Typical Output Characteristics T_j=125°C

Figure 3. Normalized On-resistance

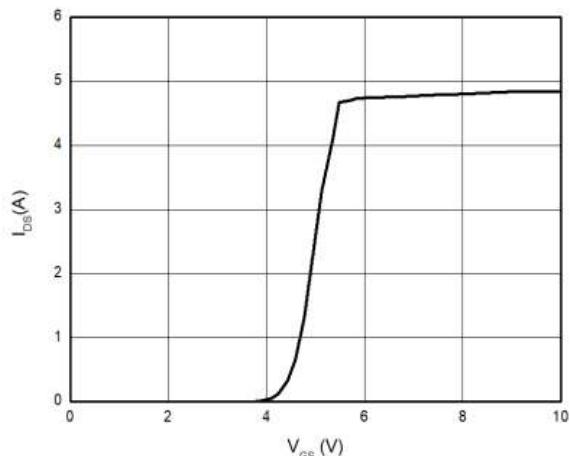
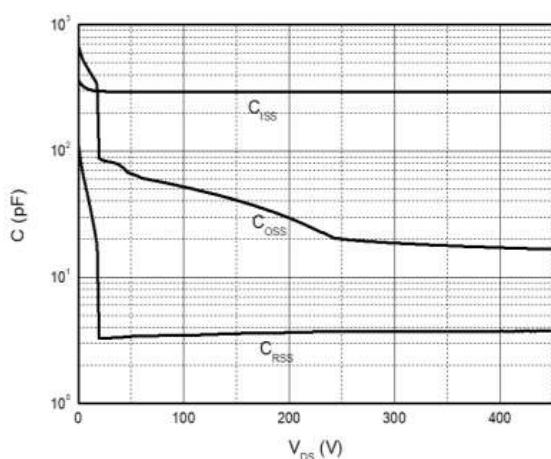
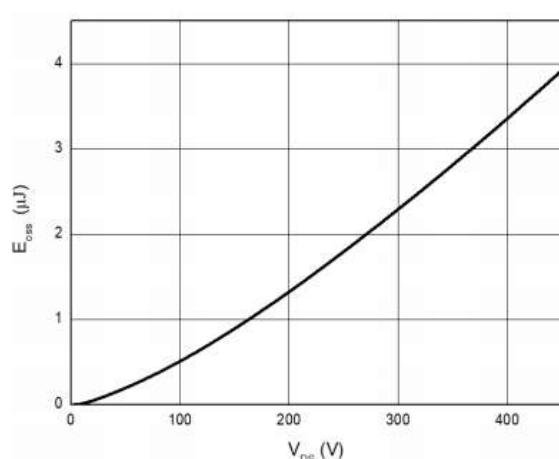
Figure 4. Typical Transfer Characteristics T_j=25°C

Figure 5. Typical Capacitance (f=1MHz)

Figure 6. Typical C_{OSS} Stored Energy

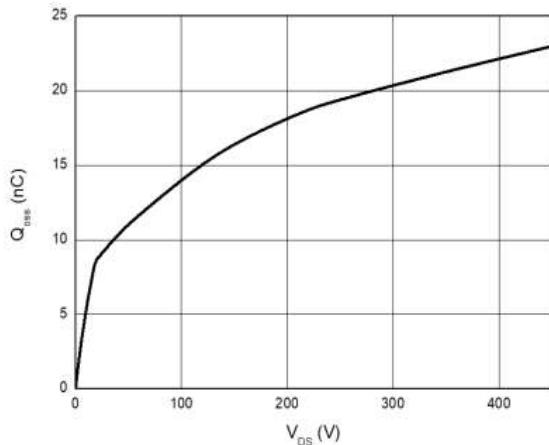


Figure 7. Typical Q_{oss}

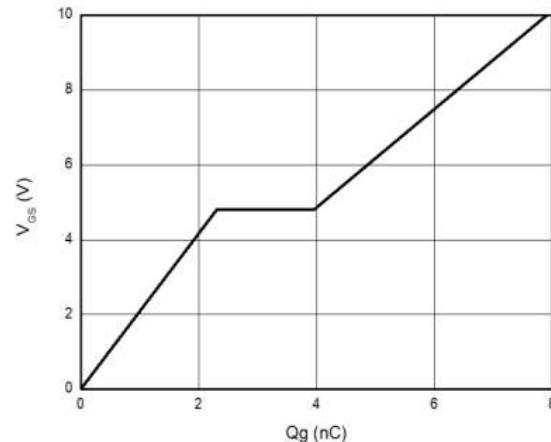


Figure 8. Typical Gate Charge ($V_{DS}=400V$, $I_D=1A$)

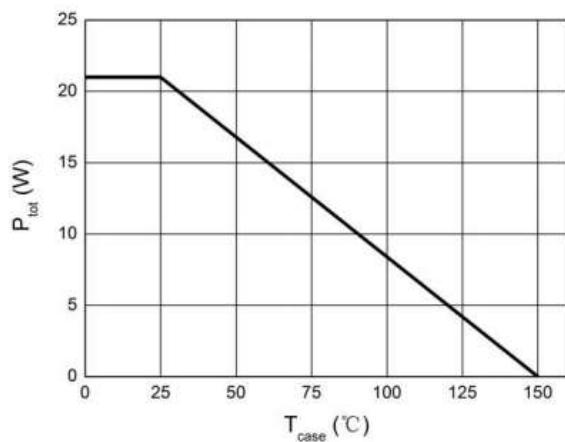
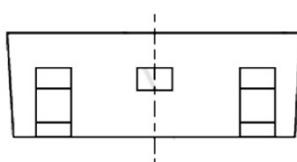
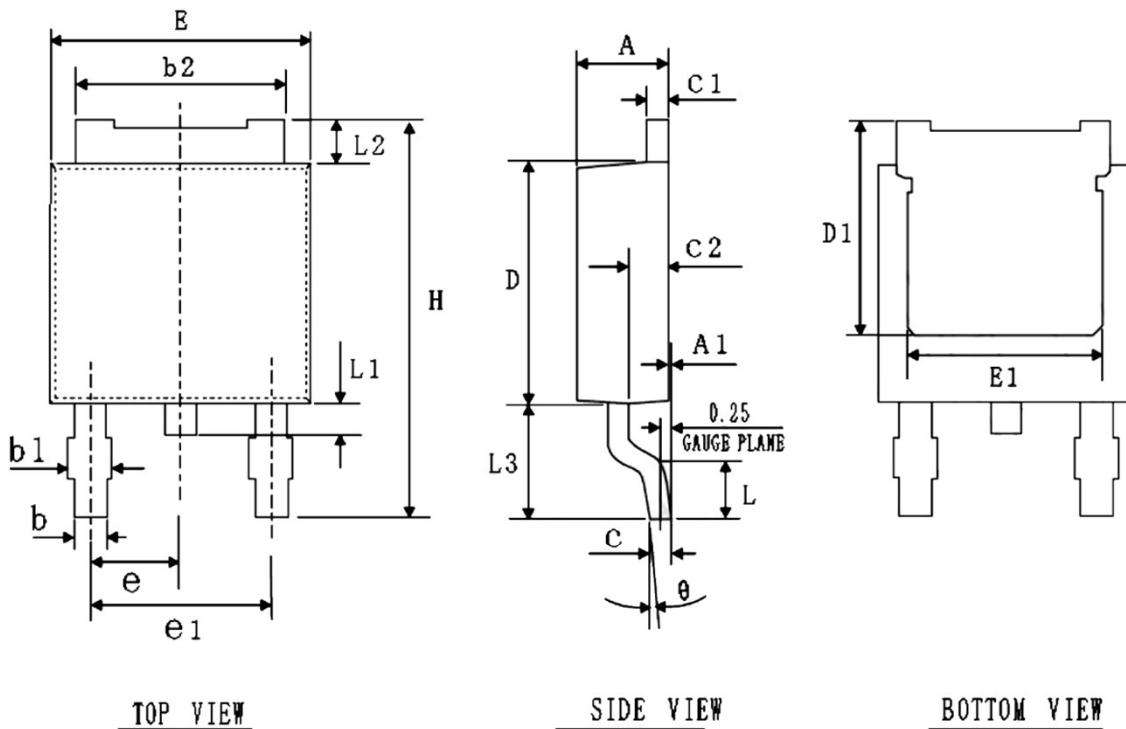


Figure 9. Power Dissipation

Product Dimension (TO-252)

**SIDE VIEW**

SYMB OL	Millimeters			SYMB OL	Millimeters			SYMB OL	Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.40	D1	5.25	5.45	5.65	θ	0°	4°	8°
A1	0.00	0.05	0.10	H	10.00	10.10	10.20	e	2.285 BSC		
b	0.762	0.812	0.862	E	6.50	6.60	6.70				
b ₁	--	--	1.10	E1	4.75	4.85	4.95				
b ₂	5.23	5.33	5.43	e1	4.37	4.57	4.77				
C	0.458	0.508	0.558	L	--	--	1.45				
C1	0.458	0.508	0.558	L1	0.60	0.75	0.90				
C2	0.80	1.00	1.20	L2	0.90	1.10	1.30				
D	6.00	6.10	6.20	L3	2.80	3.00	3.20				

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